

Figure 1

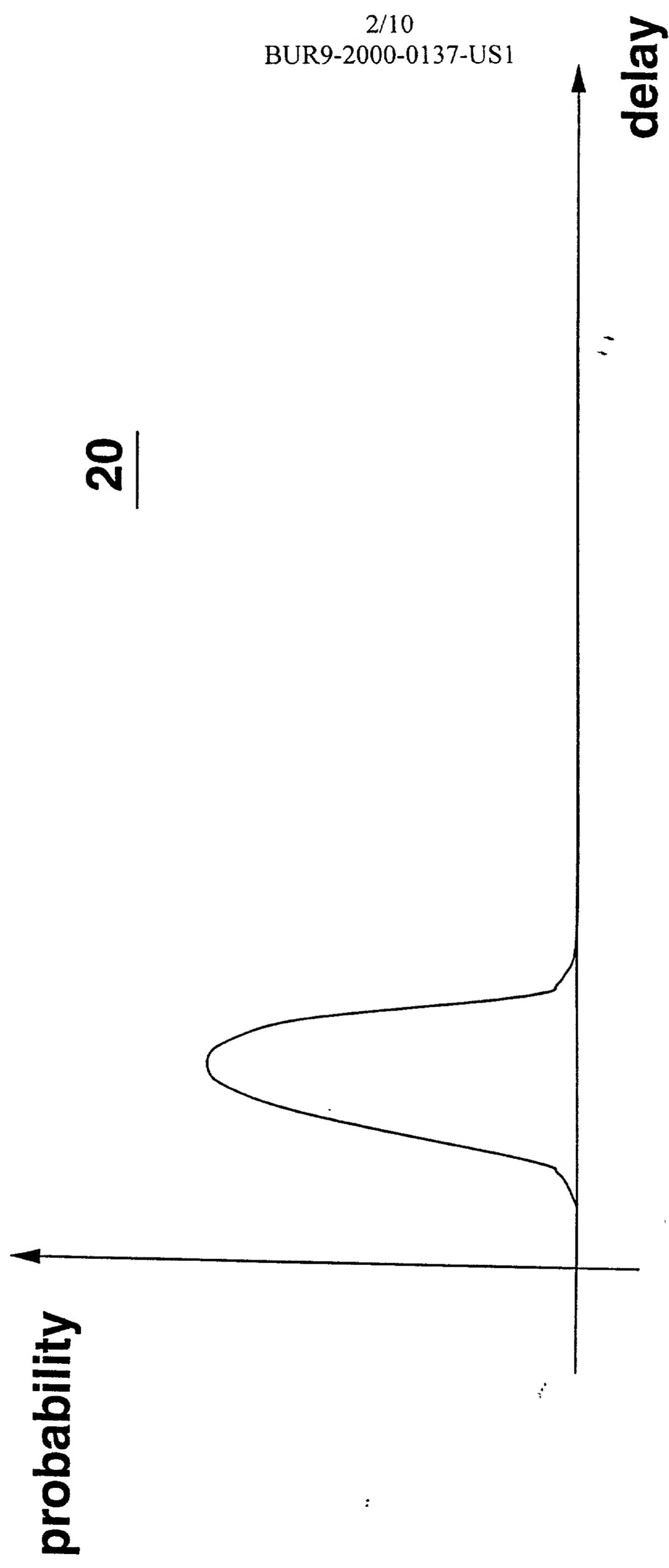


Figure 2

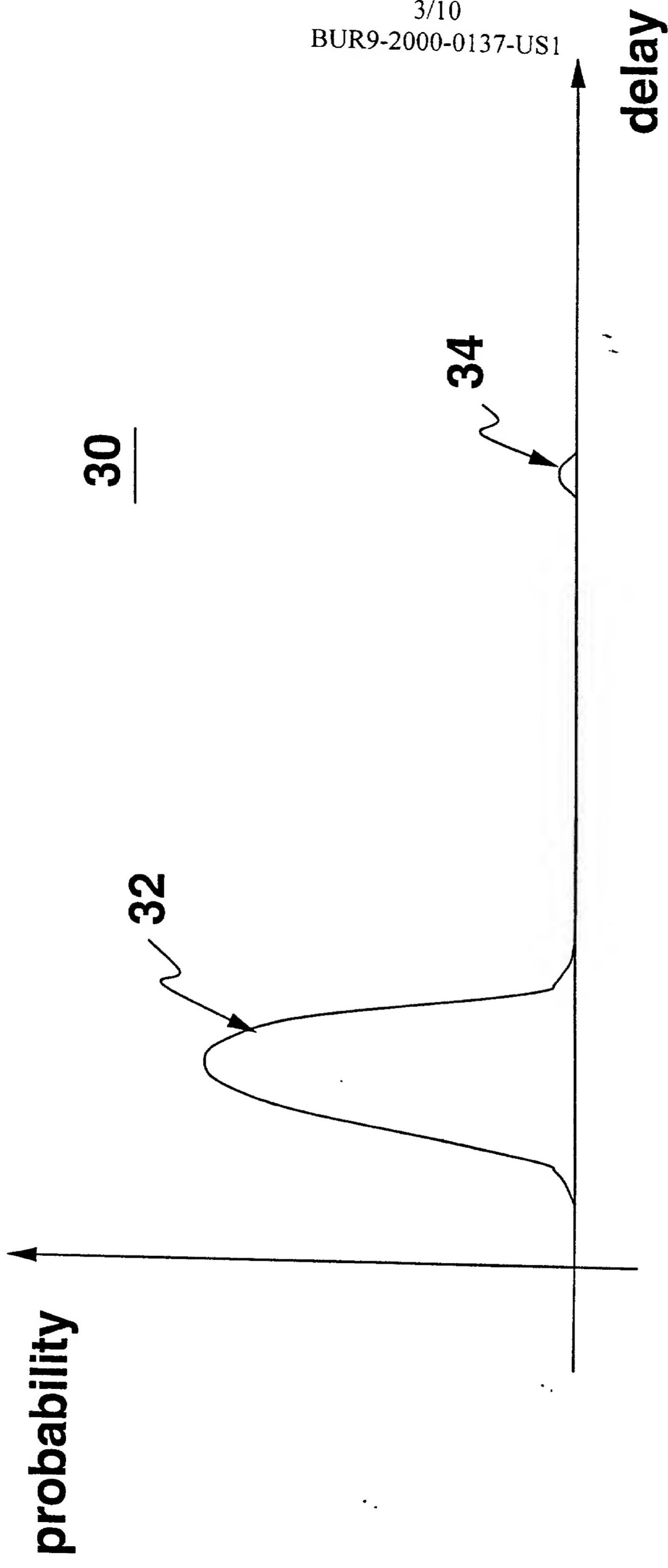


Figure 3

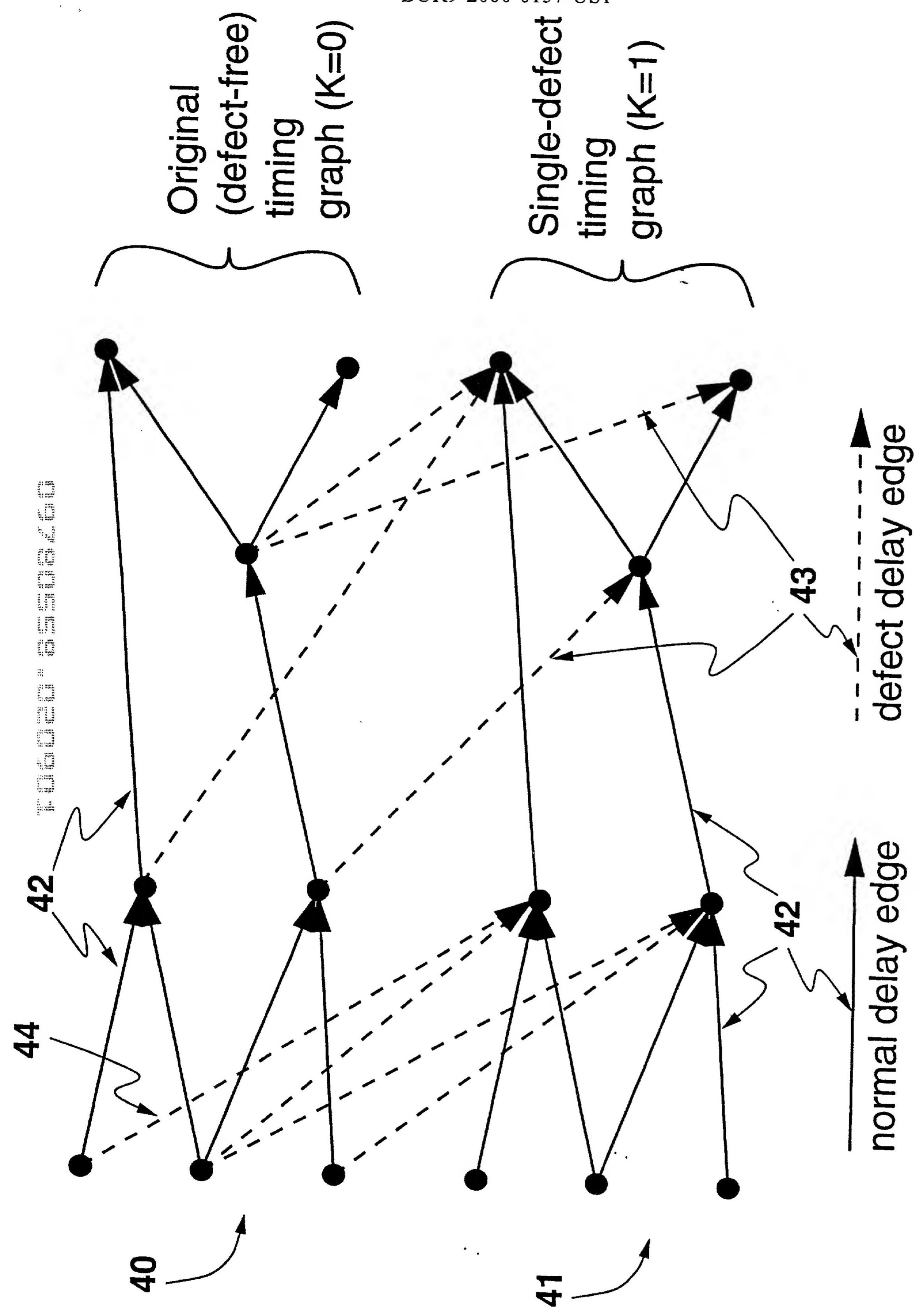


Figure 4a

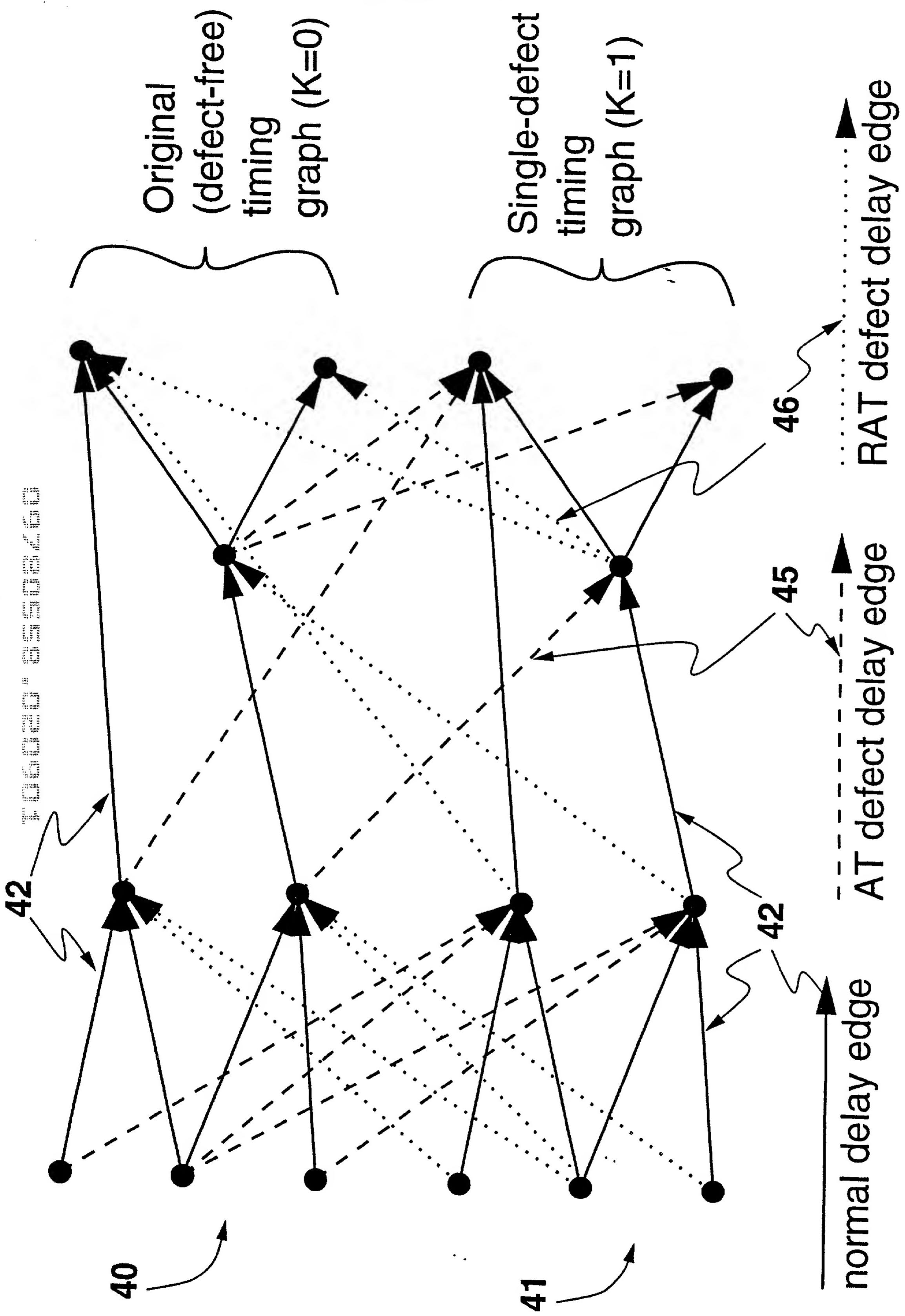


Figure 4b

50

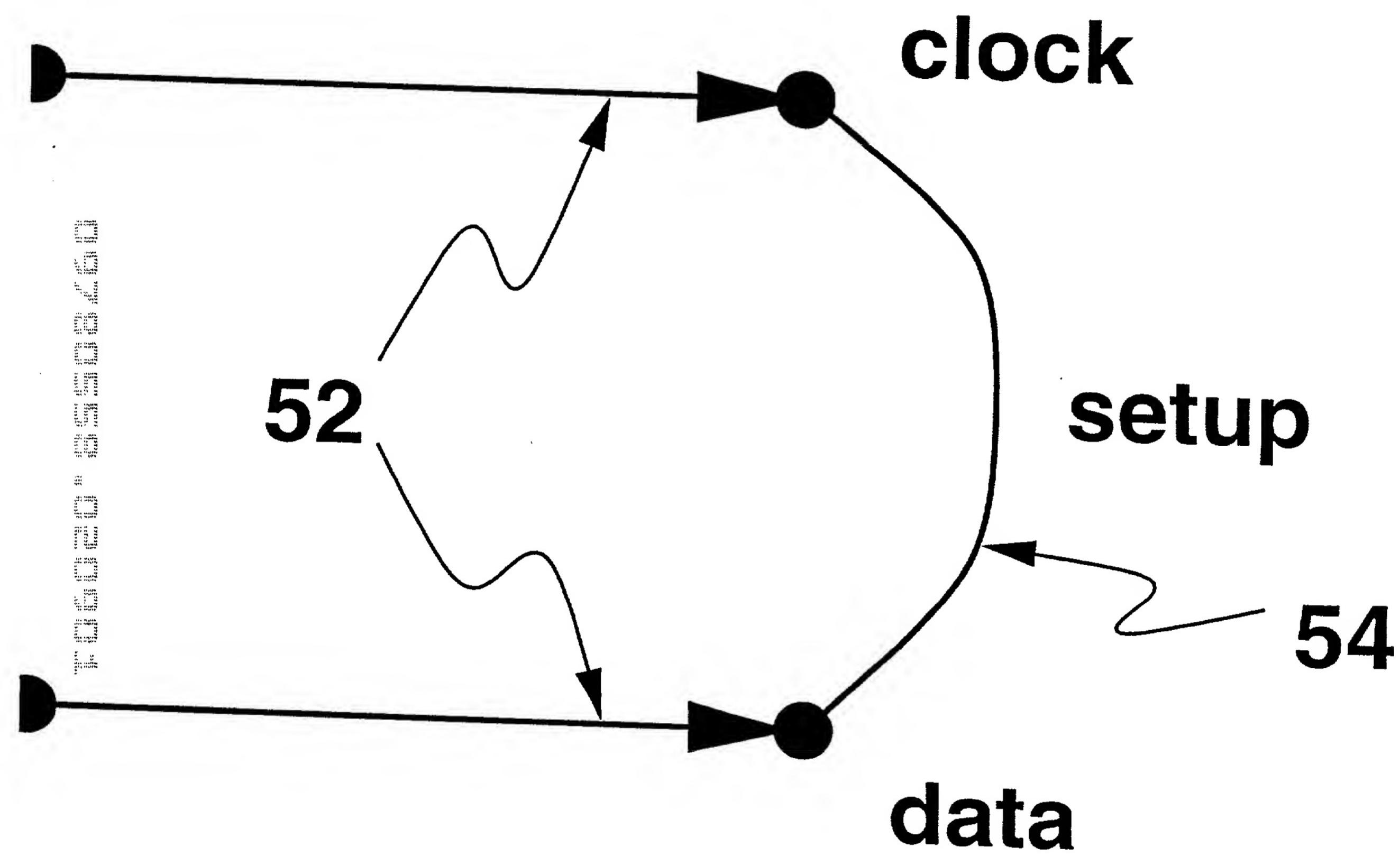


Figure 5a

7/10
BUR9-2000-0137-US1

clock



data



clock



setup



data

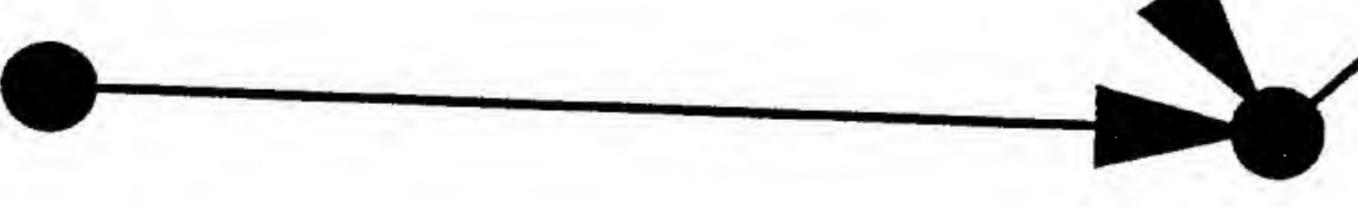


Figure 5b

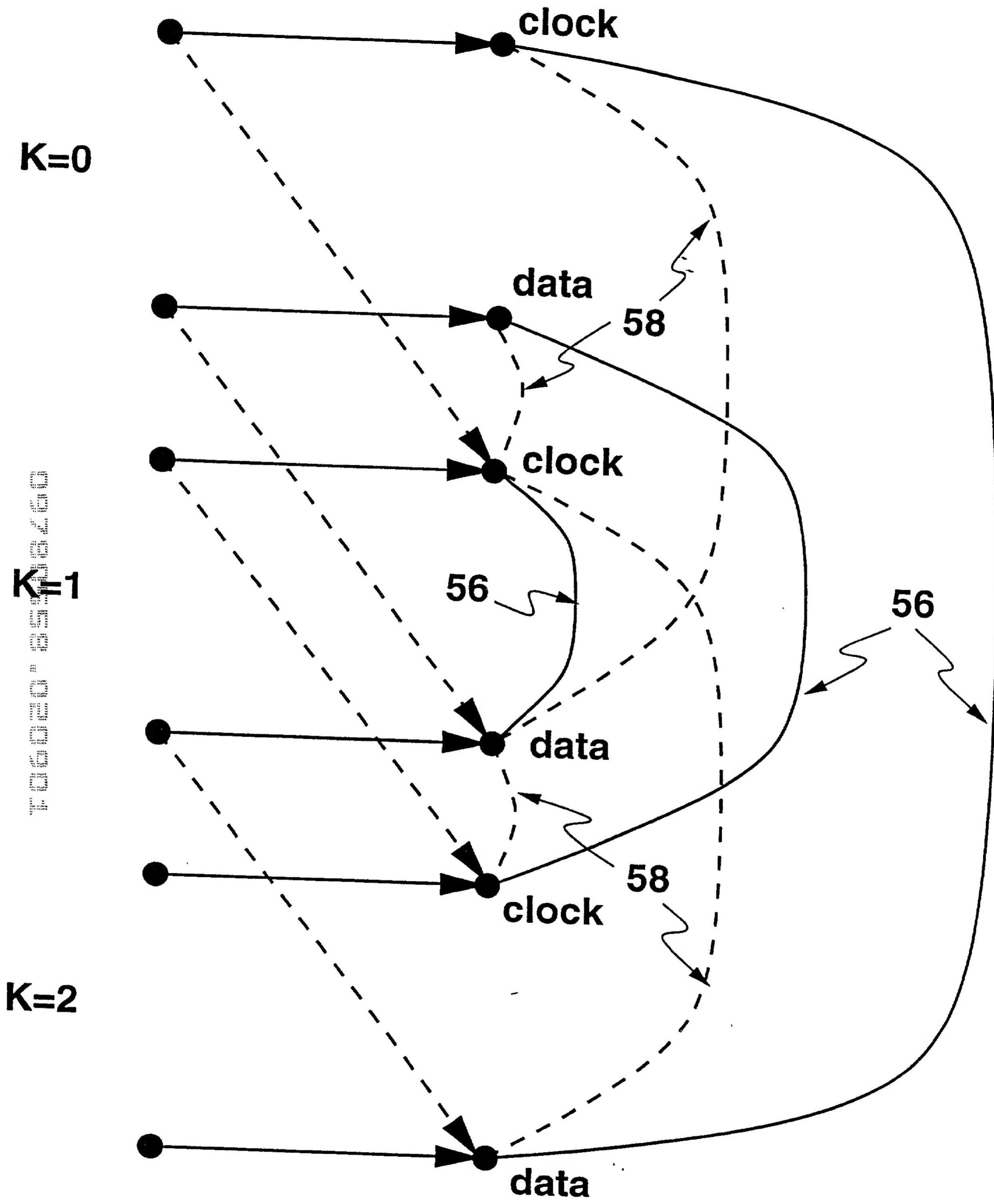


Figure 5c

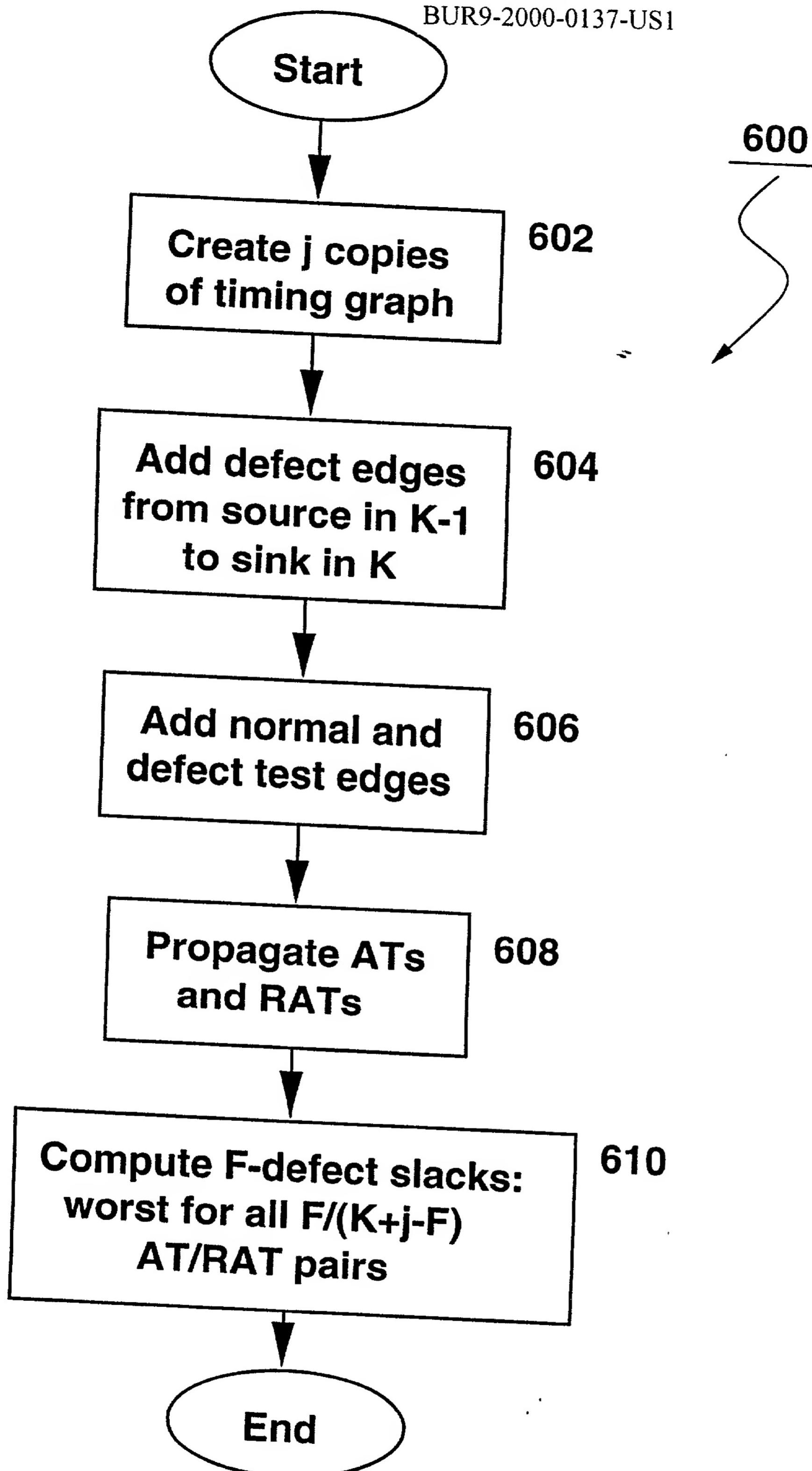


Figure 6

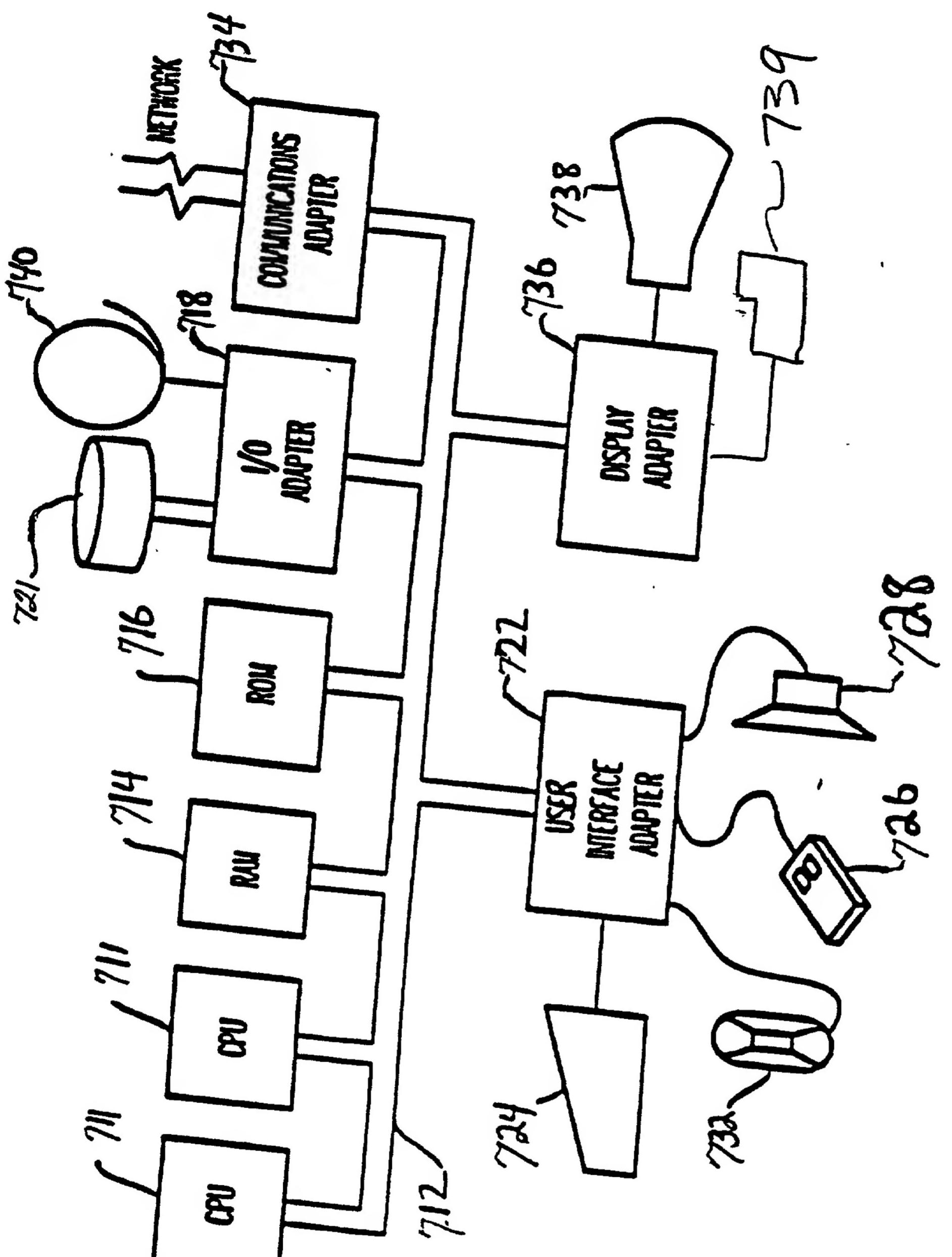


FIGURE 7

FIGURE 8

